AMENDMENT TO THE SPECIFICATION

Please amend the paragraph beginning on page 12, line 11 and ending on page 12, line 24;

FIG. 9 is simplified block diagram of a Reserve ECC encoded 900 in accordance with the present invention. Encoder 900 is similar to the prior art encoder 600 shown in FIG. 6, however, a seed is introduced at block 902 prior to calculating the ECC parity. ~~The parity does not undergo run length limited encoding as required in encoder 600 of FIG. 6.~~ As discussed above, the value of the seed can be determined through an iterative process. Specifically, at block 904, the parity is compared to the constraint requirement. If the constraint is not met, a new seed is selected at block 902. Blocks 902, 604, 606 and 904 generally provide a seed selection module. FIG. 10 is a simplified block diagram of a Reverse ECC decoder 1000 in accordance with the present invention. Decoder 1000 is similar to prior art decoder 700 shown in FIG. 7. However, the RLL parity decode is not required in decoder 1000 and there is no resultant error propagation. Instead, a seed value is introduced at block 1002 prior to calculation of the error locations at block 704.